Evaluation Transient Stability of Large Scale Power System with Multi-Terminal HVDC

Prechanon Kumkratug

Abstract — This paper presents the method of evaluating transient stability of large scale power system equipped with multi-terminal high voltage direct current (MTDC). The power system including synchronous machine, transmission network, and HVDC is based on the concepts of stability mode. In addition, various techniques to reduce the simulation time are systematically applied. The proposed method helps us to access the transient stability of the system with MTDC in the much simpler way. The verification of the methods is tested on 20 generators in an IEEE 118-bus system under various cases.

Index Terms—Power system, transient stability, HVDC, MTDC.

I. INTRODUCTION

Power system stability is to study the behaviors of the synchronous machines after disturbances. Transient stability refers to the ability of all machines in the system to remain in a state of operating equilibrium under normal operating conditions or to regain and acceptable point near equilibrium point after severe and sudden disturbance. High voltage direct current (HVDC) system consisting of two converters has the ability to rapidly control the transmitted active power flow [1]-[2]. The fast acting of power flow control is able to improve transient stability of power system [3]. By adding one or more converter, the MTDC system has a better performance of application in power systems [3]-[7].

For purposes of analyzing transient stability, it is helpful to conceptually ensure that the system must be able to continually supply electrical energy to load with the standard of frequency and voltage. For many years, the novel method to access and improve transient have been proposed by the power system researchers. Momentary and stability modes are widely accepted to verify the power system analysis. When working in momentary mode power system including MTDC system is modeled in detail. For large scale power system analysis, it is very time consuming [9]-[12]. For stability mode power system and MTDC system are modeled in the much simpler way.

This paper presents the method of analyzing transient stability of large scale power equipped with MTDC in stability mode. This paper has the following outline. Section II provides some a mathematical model of the power system and MTDC system. Section III presents the proposed method. In Section IV, the verification of the proposed control strategy and simulation method is tested on the IEEE 118-bus system with various fault occurrences and MTDC locations. Finally, conclusions are drawn in Section V.

II. BACKGROUND

First, consider the power system without MTDC. A single line diagram of a multi-machine system consisting of ng generators is shown in Fig. 1(a). Fig. 1(b) shows the equivalent circuit of Fig. 1(a). Fig. 1(c) is used for evaluating the electrical power output (P) of Fig. 1(b). Here I_G is the current injection of a synchronous generator. Y_k is the reduced admittance matrix of all physical load buses from bus ng+1 to bus n. The swing equation of a multi-machine system in the center of inertia (COI) is given by [13]-[14]

\[ \dot{\delta}_i = \dot{\alpha}_l \]  
\[ \ddot{\alpha}_k = \frac{1}{M_i} [P_i - P_{ei} - M \cdot P_{COI} - D_j \dot{\delta}_l] \quad i=1, 2...ng \]  

where

\[ P_i = P_{mi} - E_i^2 G_{ii} \]

\[ P_{ei} = \sum_{j=1, j \neq i}^{ng} (C_{ij} \sin \theta_{ij} + D_{ij} \cos \theta_{ij}) \]

\[ P_{COI} = \sum_{i=1}^{ng} (P_{mi} - C_{ij}) - 2 \sum_{i=1}^{ng} \sum_{j=i+1}^{ng} D_{ij} \cos \theta_{ij} \]

\[ M_T = \sum_{i=1}^{ng} M_i \]

\[ C_{ij} = E_i^* E_j^* B_{ij} \]

\[ D_{ij} = E_i^* E_j^* G_{ij} \]

and

\[ \theta_{ij} = \theta_i - \theta_j = \dot{\theta}_i - \dot{\theta}_j \]

G and B are the real and imaginary parts in each element of Y_k.
Consider Fig. 2, MTDC system such as 3-terminal HVDC is applied to exchange active power flow \((P_C)\) between converters. The active power balance at bus \(C_1\), \(C_2\), and \(C_3\) is given by

\[
P_{C1}^{\text{inj}} = P_{C1} + P_{LC1}
\]

(3)

\[
P_{C2}^{\text{inj}} = P_{C2} + P_{LC2}
\]

(4)

\[
P_{C3}^{\text{inj}} = P_{C3} + P_{LC3}
\]

(5)

Here \(P_{LC}\) is the active power load.

Fig. 1. A multi-machine power system: (a) single line diagram; (b) simple equivalent circuit; (c) current injection model and reduced admittance matrix representations of the system.

Fig. 2. A multi-machine power system equipped with a 3-terminal HVDC.

III. A PROPOSED METHOD

The aim of this section is to develop the simulation method for working with a large scale power system equipped with the MTDC. The proposed simulation method in the form of a flow chart is shown in Fig. 3.

A step-by-step description follows:

**Pre-fault**

**Step 1:** Perform the initial operation of the power system at pre-fault such as \(P_{uis}, E'_i, \) and \(\delta_i\).

**Step 2:** Reorder the constant transient voltage bus and MTDC bus number. To simplify the partitioned admittance matrix formulation, they are numbered consecutively. The constant transient voltage buses are numbered from bus 1 to bus \(ng\) whereas the MTDC buses are numbered from bus \(ng+1\) to \(ng+nc\). Here \(nc\) is the number of HVDC terminals.

**Step 3:** Perform \(Y_{Pr}^d\), \(Y_{R}^d\), and \(Y_{P}^d\). The superscripts ‘d’ and ‘a’ are represented by the admittance matrix at fault and post-fault, respectively. Similarly, the subscripts ‘Pr’ and ‘R’ are the admittance matrix by which the physical load buses are partially and entirely reduced, respectively. \(Y_{R}\) is in the order of \((ng \times ng)\) and \(Y_{P}\) is in the order of \((ng + nc \times ng + nc)\). \(Y_{P}\) is formulated by using the node equation given by

\[
\begin{bmatrix}
I_0 \\
0
\end{bmatrix} = \begin{bmatrix}
Y_{P1}^{d,a} & Y_{P2}^{d,a} & E' \\
Y_{P3}^{d,a} & Y_{P4}^{d,a} & V_C
\end{bmatrix}
\]

(6)

Here,

\(Y_{P1}^{d,a}\) is the order of \((ng \times ng)\);

\(Y_{P2}^{d,a}\) is the order of \((ng \times nc)\);

\(Y_{P3}^{d,a}\) is the order of \((nc \times ng)\);

\(Y_{P4}^{d,a}\) is the order of \((nc \times nc)\);

\(V_C\) is the ac vector voltage of MTDC buses.
Step 4: Declare $P_m, E_i, \delta_i, Y^d_P, Y^d_R$, and $Y^a_P$ as global variables.

Fault

Step 5: Use $Y^d_R$ to calculate the current injection of generator ($I_G$), the output of electrical power, and to evaluate the rotor angles and machine speeds from (1) and (2), respectively.

Step 6: Calculate $V_C$ as given by

$$[V_C] = \left[Y^d_{p4}\right]^{-1}\left[Y^d_{p3}\right]E$$

(7)

Step 7: Increase the step time ($\Delta t$) and repeat step (5) to (6) until the clearing fault time ($t_c$) is reached.

Post-fault

Step 8: Calculate $V_C$ at post-fault by replacing $Y^d_{p3}$ and $Y^d_{p4}$ in (21) with $Y^a_{p3}$ and $Y^a_{p4}$, respectively.

Step 9: Determine the suitable active power injection ($P_c$) depending on the control strategy.

Step 10: Incorporate the MDCT system into the ac networks. $P_c$ is converted to the current injection ($I_C$) as shown in Fig. 4(a). Adding ($I_C/V_C$) to the diagonal elements of $Y^d_{p4}$, the new $Y^d_{p4}$ is defined by $Y^d_{p4,H}$ in the node equation of

$$I_G = \begin{bmatrix} Y^d_{p1} & Y^d_{p2} \\ Y^a_{p3} & Y^a_{p4,H} \end{bmatrix} E'$$

(8)

Fig. 4. Successive method of incorporation of MTDC to power system: (a) current injection models of machine and MTDC (b) reduced admittance matrix of power system with MTDC.

Step 11: Perform $Y^a_R$ as shown in Fig. 4(b) by eliminating MTDC bus.

Step 12: Use $Y^a_R$ to calculate the current injection of generator ($I_G$), the output of electrical power, the rotor angles (1), and machine speeds (2), respectively.

Step 13: Increase the step time ($\Delta t$) and repeat step (7) to step (12) until the maximum time ($t_{max}$) is obtained.

Step 14: Reorder the MTDC buses as they appear in a single line diagram.

Step 15: Display the simulation results.

Fig. 5. Single line diagram of the IEEE 118-bus system equipped with the MTDC.

Fig. 6. Results of the system without MTDC for first case of fault occurrence: (a) voltage at 118 bus (b) voltage at MTDC bus (c) generator rotor angle.
IV. SIMULATION RESULTS

Fig. 5 shows the IEEE 118-bus system used to verify the proposed method. It consists of 32 synchronous condensers, 20 synchronous generators, and 186 transmission lines. The system data for the power flow analysis was reported in [15]. The data of the 20 synchronous generators for the transient stability analysis was reported in [16]. The MTDC is arbitrary in the center of the system: Firstly, it is considered that a temporary three phase fault occurs at bus 84. Fig. 6 shows the results of the system without MTDC for clearing time \((t_f)=112\ \text{msec}\). The system is considered as unstable. Fig. 7 shows the system with \(t_f=113\ \text{msec}\). The system is considered unstable. In this case of fault occurrence, the critical clearing time is 112-113 \(\text{msec}\). However, with MTDC, the system can increase the robustness of the system as shown in Fig. 8(a). The active power flow control is shown in Fig. 8(b). The second case study is to assume that three phase fault appears at bus 15. The swing curve is shown in Fig. 9. It can be observed in the Fig. 9(a) and Fig. 9(b) the MTDC can enhance transient stability of large scale power system.

![Fig. 7. Generator rotor angle of the system for second case.](image)

![Fig. 8. The system with MTDC for the third case: (a) generator rotor angle (b) active power injection of MTDC.](image)

![Fig. 9. Results of the system for second case of fault occurrence: (a) without MTDC (b) with MTDC.](image)

V. CONCLUSION

The proposed simulation method for evaluating the transient stability of a large scale power system equipped with MTDC is proposed in this paper. All the constant values and matrices are performed and declared as global variables at steady state analysis. During the transient stability analysis, the control of the MTDC will only change the diagonal of the partially reduced admittance matrix of the system.

The main reasons behind the fast simulation in this proposed method are:

1) Working with much smaller matrix dimensions. It is time consuming by using the structure preserving network model as shown in Fig. 2. The matrix dimension in this model is the order of \((n \times n)\). The proposed method deals with the structure reduced network model as shown in Fig. 4. The maximum matrix dimension is reduced to the order of \((ng + nc + nc)\).

2) Performing the partially reduced admittance matrix and declaring it as a global variable before evaluating the transient stability of the system. Its diagonal of \(Y_{d4}^{cl}\) which is a small matrix dimension \((nc + nc)\) can potentially be modified when the MTDC is controlled at the post fault.
REFERENCES


Prechanon Kumkratug received his B.E., M.E., and D.E. degrees from King Mongkut’s Institute of Technology Ladkrabang, in 1996. Asian Institute of Technology, in 1999, and Kasetsart University at Bang Khean Campus, in 2008, respectively. His employment experience included the Advanced Info Services plc, Mahanakorn University, King Mongkut’s Institute of Technology Thonburi, and Eastern Asian University. He is currently employed as an Associate Professor at the Department of Electrical and Electronics Engineering, Kasetsart University, Sri Racha Campus.